

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	510	"379"/\$.ccls. and (bidirectional or bi-directional or two-way) SAME (transducer or speaker or loudspeaker)	US-PGPUB; USPAT	OR	ON	2005/04/16 13:27
L2	52	"379"/\$.ccls. and (bidirectional or bi-directional or two-way) adj3 (transducer or speaker or loudspeaker)	US-PGPUB; USPAT	OR	ON	2005/04/16 13:27
S1	0	"381"/\$.ccls. and sound adj3 processing WITH interface WITH speaker	USPAT	OR	ON	2001/10/17 07:23
S2	3	"381"/\$.ccls. and sound adj3 processing WITH interface	USPAT	OR	ON	2001/03/26 10:01
S3	0	"381"/\$.ccls. and sound adj3 processing adj3 system WITH integrated adj3 circuit	USPAT	OR	ON	2001/03/26 10:04
S4	0	"381"/\$.ccls. and sound adj3 processing adj3 system SAME integrated adj3 circuit	USPAT	OR	ON	2001/03/26 10:04
S5	0	"381"/\$.ccls. and sound adj3 processing adj3 system SAME multiplexing adj3 circuit	USPAT	OR	ON	2001/03/26 10:04
S6	0	"381"/\$.ccls. and sound adj3 processing adj3 system WITH multiplexing adj3 circuit	USPAT	OR	ON	2001/03/26 10:08
S7	5	"381"/\$.ccls. and sound adj3 processing adj3 system WITH speaker	USPAT	OR	ON	2001/03/27 14:27
S8	0	"340"/\$.ccls. and sound adj3 processing adj3 system WITH multiplexing adj3 circuit	USPAT	OR	ON	2001/03/26 10:09
S9	0	sound adj3 processing adj3 system WITH multiplexing	USPAT	OR	ON	2001/03/26 10:12
S10	44	speakers WITH multiplexing	USPAT	OR	ON	2001/03/28 09:14
S11	0	speakers WITH multiplexing WITH integrated adj3 circuit	USPAT	OR	ON	2001/03/26 10:27
S12	2	speakers WITH multiplexing WITH IC	USPAT	OR	ON	2001/03/26 10:28
S13	0	"381"/\$.ccls. and sound adj3 processing adj3 system WITH speaker SAME integrated adj3 circuit	USPAT	OR	ON	2001/03/27 14:28
S14	0	"381"/\$.ccls. and sound adj3 processing adj3 system SAME integrated adj3 circuit	USPAT	OR	ON	2001/03/27 14:29

S15	2	sound adj3 processing adj3 system SAME integrated adj3 circuit	USPAT	OR	ON	2001/03/27 14:32
S16	0	speakers WITH multiplexing WITH integrated adj3 circuit	USPAT	OR	ON	2001/03/28 09:20
S17	382	speakers WITH integrated adj3 circuit	USPAT	OR	ON	2001/03/28 09:30
S18	0	speakers WITH integrated adj3 circuit SAME single adj3 pin	USPAT	OR	ON	2001/03/28 09:21
S19	325	integrated adj3 circuit SAME single adj3 pin	USPAT	OR	ON	2001/03/28 09:27
S20	1	integrated adj3 circuit SAME single adj3 pin WITH audio	USPAT	OR	ON	2001/03/28 09:25
S21	512	integrated adj3 circuit SAME input adj3 output adj3 pin	USPAT	OR	ON	2001/03/28 09:30
S22	0	(speakers WITH integrated adj3 circuit) and (speakers WITH (output SAME input) adj3 circuits)	USPAT	OR	ON	2001/03/28 09:34
S23	14	speakers WITH (output SAME input) adj3 circuits	USPAT	OR	ON	2001/03/28 09:50
S24	0	speakers WITH (output-input) adj3 circuits	USPAT	OR	ON	2001/03/28 09:54
S25	0	speakers WITH bi-directional adj3 integrated adj3 circuits	USPAT	OR	ON	2001/03/28 09:54
S26	37	bi-directional adj3 integrated adj3 circuits	USPAT	OR	ON	2001/03/28 13:04
S27	0	bi-directional adj3 integrated adj3 circuits WITH sound	USPAT	OR	ON	2001/03/28 10:12
S28	0	bi-directional adj3 integrated adj3 circuits WITH audio	USPAT	OR	ON	2001/03/28 10:13
S29	0	"381"/\$.ccls. and bi-directional adj3 integrated adj3 circuits	USPAT	OR	ON	2001/03/28 10:14
S30	1	"455"/\$.ccls. and bi-directional adj3 integrated adj3 circuits	USPAT	OR	ON	2001/03/28 10:14
S31	0	bi-directional adj3 transducer WITH integrated adj3 circuit	USPAT	OR	ON	2001/03/28 13:05
S32	73	bi-directional adj3 transducer	USPAT	OR	ON	2001/03/28 13:09
S33	0	bi-directional adj3 transducer WITH sound adj3 systems	USPAT	OR	ON	2001/03/28 13:10
S34	3	bi-directional adj3 transducer WITH sound	USPAT	OR	ON	2001/03/28 13:10
S35	47	integrated adj3 circuit SAME sound SAME speaker	EPO; JPO	OR	ON	2001/10/15 18:56
S36	367	integrated adj3 circuit SAME sound SAME speaker	USPAT	OR	ON	2001/10/15 18:57
S37	41	"381"/\$.ccls. and integrated adj3 circuit SAME sound SAME speaker	USPAT	OR	ON	2001/10/15 18:57

S38	189	"381"/\$.ccls. and interface WITH sound	USPAT	OR	ON	2001/10/16 18:03
S39	6	"381"/\$.ccls. and bi-directional adj3 interface	USPAT	OR	ON	2001/10/16 18:05
S40	24	"381"/\$.ccls. and bi-directional WITH sound	USPAT	OR	ON	2001/10/16 18:08
S41	1	"381"/\$.ccls. and bi-directional WITH integrated adj3 circuit	USPAT	OR	ON	2001/10/16 18:09
S42	76963	bidirectional adj3 pin SAME integrate\$3 adj3 circuit or IC	USPAT	OR	ON	2001/10/17 07:25
S43	76933	"375"/\$.ccls. and bidirectional adj3 pin SAME integrate\$3 adj3 circuit or IC	USPAT	OR	ON	2001/10/17 07:26
S44	42	bidirectional adj3 pin SAME integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 07:34
S45	13	bidirectional adj3 pin WITH integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 07:27
S46	1	bidirectional adj3 pin SAME integrate\$3 adj3 circuit SAME audio	USPAT	OR	ON	2001/10/17 07:34
S47	0	bidirectional adj3 pin SAME integrate\$3 adj3 circuit SAME sound	USPAT	OR	ON	2001/10/17 07:34
S48	0	bidirectional adj3 pin SAME integrate\$3 adj3 circuit SAME speaker	USPAT	OR	ON	2001/10/17 07:34
S49	0	bidirectional adj3 pin SAME integrate\$3 adj3 circuit SAME transducer	USPAT	OR	ON	2001/10/17 07:35
S50	0	bidirectional adj3 pin SAME multplexing	USPAT	OR	ON	2001/10/17 07:35
S51	0	bidirectional adj3 pin SAME speaker	USPAT	OR	ON	2001/10/17 07:35
S52	0	bidirectional adj3 pin SAME loudspeaker	USPAT	OR	ON	2001/10/17 07:36
S53	0	bi-directional adj3 pin SAME loudspeaker	USPAT	OR	ON	2001/10/17 07:36
S54	2	bi-directional adj3 loudspeaker	USPAT	OR	ON	2001/10/17 07:37
S55	3	bidirectional adj3 loudspeaker	USPAT	OR	ON	2001/10/17 07:39
S56	793796	bidirectional adj3 speaker SAME integrat\$3 circuit	USPAT	OR	ON	2001/10/17 07:40
S57	0	"381"/\$.ccls. and bidirectional adj3 speaker SAME integrat\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 07:41
S58	7	"381"/\$.ccls. and bidirectional adj3 speaker	USPAT	OR	ON	2001/10/17 07:41
S59	99	"381"/\$.ccls. and memory SAME integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 13:50

S60	1	381/120.ccls. and memory SAME integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 13:52
S61	99	"381"/\$.ccls. and memory SAME integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 13:53
S62	0	381/59.ccls. and memory SAME integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 13:53
S63	1	381/111.ccls. and memory SAME integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 13:55
S64	1	381/300.ccls. and memory SAME integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 13:56
S65	7	700/94.ccls. and memory SAME integrate\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 13:59
S66	8858	memory SAME integrate\$3 adj3 circuit SAME read	USPAT	OR	ON	2001/10/17 13:59
S67	3928	memory SAME integrate\$3 adj3 circuit SAME ROM	USPAT	OR	ON	2001/10/17 14:00
S68	1486	memory WITH integrate\$3 adj3 circuit WITH ROM	USPAT	OR	ON	2001/10/17 14:20
S69	138030	700/94.ccls. and memory WITH ROM or RAM	USPAT	OR	ON	2001/10/17 14:02
S70	23	700/94.ccls. and memory WITH ROM	USPAT	OR	ON	2001/10/17 14:02
S71	28	700/94.ccls. and memory WITH RAM	USPAT	OR	ON	2001/10/17 14:02
S72	20	(700/94.ccls. and memory WITH ROM) and (700/94.ccls. and memory WITH RAM)	USPAT	OR	ON	2001/10/17 14:09
S73	27	"381"/\$.ccls. and speaker WITH input adj3 output adj3 terminal	USPAT	OR	ON	2001/10/17 14:17
S74	1	"704"/\$.ccls. and speaker WITH activat\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 14:19
S75	0	"704"/\$.ccls. and loudspeaker WITH activat\$3 adj3 circuit	USPAT	OR	ON	2001/10/17 14:19
S76	5	"381"/\$.ccls. and memory WITH integrate\$3 adj3 circuit WITH ROM	USPAT	OR	ON	2001/10/17 14:23
S77	67	"381"/\$.ccls. and read\$3 adj3 memory WITH writ\$3 adj3 memory	USPAT	OR	ON	2001/10/17 14:30
S78	51	"381"/\$.ccls. and memory WITH integrat\$3 adj circuit	USPAT	OR	ON	2001/10/17 14:26
S79	8	("381"/\$.ccls. and read\$3 adj3 memory WITH writ\$3 adj3 memory) and ("381"/\$.ccls. and memory WITH integrat\$3 adj circuit)	USPAT	OR	ON	2001/10/17 14:26
S80	144	"704"/\$.ccls. and read\$3 adj3 memory WITH writ\$3 adj3 memory	USPAT	OR	ON	2001/10/17 14:31

S81	0	704/520\$.ccls. and read\$3 adj3 memory WITH writ\$3 adj3 memory	USPAT	OR	ON	2001/10/17 14:31
S82	0	704/520.ccls. and read\$3 adj3 memory WITH writ\$3 adj3 memory	USPAT	OR	ON	2001/10/17 14:32
S83	3	704/504.ccls. and read\$3 adj3 memory WITH writ\$3 adj3 memory	USPAT	OR	ON	2001/10/17 14:34
S84	0	704/504.ccls. and voice adj3 activation	USPAT	OR	ON	2001/10/17 14:34
S85	49	"704"/\$.ccls. and voice adj3 activation	USPAT	OR	ON	2001/10/17 14:35
S86	5	"704"/\$.ccls. and voice adj3 activation SAME speaker	USPAT	OR	ON	2001/10/17 14:36
S87	0	"704"/\$.ccls. and voice adj3 activation SAME loudspeaker	USPAT	OR	ON	2001/10/17 14:37
S88	5163	"704"/\$.ccls. and speech activation SAME loudspeaker	USPAT	OR	ON	2001/10/17 14:37
S89	187	704/504\$.ccls. and speech activation SAME loudspeaker	USPAT	OR	ON	2001/10/17 14:37
S90	187	704/504.ccls. and speech activation SAME loudspeaker	USPAT	OR	ON	2001/10/17 14:37
S91	0	704/504.ccls. and speech adj3 activation SAME loudspeaker	USPAT	OR	ON	2001/10/17 14:37
S92	11	704/272.ccls. and activat\$3 adj3 circuit	USPAT	OR	ON	2001/10/18 14:56
S93	1	704/270.ccls. and voice adj3 activat\$3 adj3 circuit	USPAT	OR	ON	2001/10/18 14:48
S94	0	704/272.ccls. and automatic SAME activat\$3 adj3 circuit	USPAT	OR	ON	2001/10/18 14:49
S95	0	704/272.ccls. and automatic SAME voice adj3 activat\$3	USPAT	OR	ON	2001/10/18 14:49
S96	11	704/272.ccls. and voice adj3 activat\$3	USPAT	OR	ON	2001/10/18 14:49
S97	87	"40"/\$.ccls. and activat\$3 adj3 circuit	USPAT	OR	ON	2001/10/18 14:58
S98	18194	"40"/\$.ccls. and activat\$3 adj3 circuit SAME audio or music	USPAT	OR	ON	2001/10/18 14:59
S99	7	"40"/\$.ccls. and activat\$3 adj3 circuit SAME audio	USPAT	OR	ON	2001/10/18 15:09
S10 0	22	"369"/\$.ccls. and activat\$3 adj3 circuit SAME audio	USPAT	OR	ON	2001/10/18 15:05
S10 1	7	"40"/\$.ccls. and activat\$3 adj3 circuit SAME music	USPAT	OR	ON	2001/10/18 15:05
S10 2	8	"5671555"	USPAT	OR	ON	2001/10/18 16:19
S10 3	12	"5452274"	USPAT	OR	ON	2001/10/18 16:20

S10 4	25	sound adj3 activat\$3 SAME integrat\$3 adj3 circuit	USPAT	OR	ON	2001/10/18 16:28
S10 5	111	sound adj3 activat\$3 SAME speaker	USPAT	OR	ON	2001/10/18 16:29
S10 6	10	"381"/\$.ccls. and sound adj3 activat\$3 SAME speaker	USPAT	OR	ON	2001/10/18 16:36
S10 7	0	integrate\$3 adj3 circuit WITH audio WITH in adj3 out WITH ports	USPAT	OR	OFF	2003/03/10 14:05
S10 8	1	integrate\$3 adj3 circuit WITH input-output WITH audio	USPAT	OR	OFF	2003/03/10 14:07
S10 9	298	integrate\$3 adj3 circuit WITH input-output	USPAT	OR	OFF	2003/03/10 14:07
S11 0	0	"381"/\$.ccls. and integrate\$3 adj3 circuit WITH input-output	USPAT	OR	OFF	2003/03/10 14:07
S11 1	1	"379"/\$.ccls. and integrate\$3 adj3 circuit WITH input-output	USPAT	OR	OFF	2003/03/10 14:09
S11 2	0	700/94.ccls. and integrate\$3 adj3 circuit WITH input-output	USPAT	OR	OFF	2003/03/10 14:09
S11 3	6	"375"/\$.ccls. and integrate\$3 adj3 circuit WITH input-output	USPAT	OR	OFF	2003/03/10 14:10
S11 4	28	integrate\$3 adj3 circuit WITH input-output SAME computer	USPAT	OR	OFF	2003/03/10 14:12
S11 5	0	704/500-504.ccls. and integrate\$3 adj3 circuit WITH input-output SAME computer	USPAT	OR	OFF	2003/03/10 14:18
S11 6	10	700/94.ccls. and semiconductor WITH audio	USPAT	OR	OFF	2003/03/10 14:21
S11 7	0	semiconductor WITH audio SAME Input-output	USPAT	OR	OFF	2003/03/10 14:24
S11 8	0	bi-directional adj3 integrated adj3 circuit SAME audio	USPAT	OR	OFF	2003/03/10 14:25
S11 9	4	bi-directional adj3 integrated adj3 circuit SAME interface	USPAT	OR	OFF	2003/03/10 14:25
S12 0	42	bi-directional adj3 integrated adj3 circuit	USPAT	OR	OFF	2003/03/10 14:26
S12 1	7	"381"/\$.ccls. and bidirectional adj3 interface	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/05/13 17:18
S12 2	0	700/94.ccls. and bidirectional adj3 interface	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/05/13 17:19
S12 3	118	700/94.ccls. and (loudspeaker or speaker) SAME interface	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/05/13 17:19
S12 4	65	700/94.ccls. and (loudspeaker or speaker) WITH interface	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/05/13 17:22

S12 5	7	381/111.ccls. and (loudspeaker or speaker) WITH interface	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/05/13 17:25
S12 6	6	381/115-117.ccls. and (loudspeaker or speaker) WITH interface	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/05/13 17:25
S12 7	8	381/111.ccls. and (integrated adj3 circuit or IC) SAME (loudspeaker or speaker)	USPAT	OR	ON	2004/05/14 08:26
S12 8	7	381/115-117.ccls. and (integrated adj3 circuit or IC) SAME (loudspeaker or speaker)	USPAT	OR	ON	2004/05/14 08:30
S12 9	11	700/94.ccls. and (integrated adj3 circuit or IC) SAME (loudspeaker or speaker)	USPAT	OR	ON	2004/05/14 08:33
S13 0	308	"379"/\$.ccls. and (integrated adj3 circuit or IC) SAME (loudspeaker or speaker)	USPAT	OR	ON	2004/05/14 08:34
S13 1	110	"379"/\$.ccls. and input SAME output SAME (integrated adj3 circuit or IC) SAME (loudspeaker or speaker)	USPAT	OR	ON	2004/05/14 08:34
S13 2	14	"379"/\$.ccls. and input WITH output WITH (integrated adj3 circuit or IC) WITH (loudspeaker or speaker)	USPAT	OR	ON	2004/05/14 08:44
S13 3	0	700/94.ccls. and input WITH output WITH (integrated adj3 circuit or IC) WITH (loudspeaker or speaker)	USPAT	OR	ON	2004/05/14 09:27
S13 4	0	700/94.ccls. and input WITH output WITH (integrated adj3 circuit or IC) SAME bi-directional	USPAT	OR	ON	2004/05/14 09:28
S13 5	0	"381"/\$.ccls. and input WITH output WITH (integrated adj3 circuit or IC) SAME bi-directional	USPAT	OR	ON	2004/05/14 09:34
S13 6	0	"381"/\$.ccls. and (integrated adj3 circuit or IC) SAME bi-directional adj3 pin	USPAT	OR	ON	2004/05/14 09:35
S13 7	52	(integrated adj3 circuit or IC) SAME bi-directional adj3 pin	USPAT	OR	ON	2004/05/14 09:36
S13 8	0	(speaker or loudspeaker) SAME (integrated adj3 circuit or IC) SAME bi-directional adj3 pin	USPAT	OR	ON	2004/05/14 09:35
S13 9	0	(transducer) SAME (integrated adj3 circuit or IC) SAME bi-directional adj3 pin	USPAT	OR	ON	2004/05/14 09:36
S14 0	38	(integrated adj3 circuit or IC) WITH bi-directional adj3 pin	USPAT	OR	ON	2004/05/14 09:45

S14 1	1	("5898703").PN.	USPAT; USOCR	OR	OFF	2004/05/14 10:40
S14 2	0	381/59.ccls. and dual adj3 transducer	USPAT	OR	OFF	2004/05/14 10:41
S14 3	11	381/59.ccls. and (speaker or loudspeaker) SAME microphone SAME transducer	USPAT	OR	OFF	2004/05/14 10:42
S14 4	11	381/59.ccls. and (speaker or loudspeaker) SAME microphone SAME transducer	USPAT	OR	OFF	2004/05/14 10:42
S14 5	11	381/59.ccls. and (speaker or loudspeaker) SAME microphone SAME transducer	USPAT; EPO; JPO	OR	OFF	2004/05/14 10:45
S14 6	15	381/96.ccls. and (speaker or loudspeaker) SAME microphone SAME transducer	USPAT; EPO; JPO	OR	OFF	2004/05/14 11:00
S14 7	1	381/96.ccls. and bilateral SAME transducer	USPAT; EPO; JPO	OR	OFF	2004/05/14 11:01
S14 8	0	381/59.ccls. and bilateral SAME transducer	USPAT; EPO; JPO	OR	OFF	2004/05/14 11:01
S14 9	9	"381"/\$.ccls. and bilateral SAME transducer	USPAT; EPO; JPO	OR	OFF	2004/05/14 11:02
S15 0	31	381/110.ccls. and speaker SAME level	USPAT	OR	ON	2004/05/16 18:47
S15 1	17	381/110.ccls. and speaker WITH level	USPAT	OR	ON	2004/05/16 18:49
S15 2	4	381/110.ccls. and speaker WITH threshold	USPAT	OR	ON	2004/05/16 18:49
S17 6	136	(3-pin or (three adj2 pin)) with (IC or (integrated adj2 circuit))	USPAT; EPO; JPO	OR	ON	2005/04/12 15:39
S17 7	1	(3-pin or (three adj2 pin)) with (IC or (integrated adj2 circuit)) same audio	USPAT; EPO; JPO	OR	ON	2005/04/12 15:40
S17 8	1	(3-pin or (three adj2 pin)) same (IC or (integrated adj2 circuit)) same audio	USPAT; EPO; JPO	OR	ON	2005/04/12 15:40
S17 9	1	(3-pin or (three adj2 pin)) same (IC or (integrated adj2 circuit)) same audio	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:41
S18 0	26	(3-pin or (three adj2 pin)) same (IC or (integrated adj2 circuit)) and audio	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:41
S18 1	0	(3-pin or (three adj2 pin)) same (IC or (integrated adj2 circuit)) same \$4speaker	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:42
S18 2	0	(3-pin or (three adj2 pin)) same (IC or (integrated adj2 circuit)) same (loudspeaker or speaker)	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:42

S18 3	3880	(IC or (integrated adj2 circuit)) same (loudspeaker or speaker)	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:43
S18 4	0	(381/311).ccls. and (IC or (integrated adj2 circuit)) same (loudspeaker or speaker)	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:43
S18 5	18	381/111-117.ccls. and (IC or (integrated adj2 circuit)) same (loudspeaker or speaker)	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:45
S18 6	0	"T092" adj3 pin	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:46
S18 7	18	"T092"	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:48
S18 8	0	"T092" same (IC or (integrated adj2 circuit) or (integrated adj2 chip))	USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/12 15:49
S18 9	0	"381"/\$.ccls. and three adj3 pin adj3 ((integrat\$3 or circuit) or IC)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/13 10:34
S19 0	49	"381"/\$.ccls. and three adj3 pin	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/13 10:36
S19 1	2	"381"/\$.ccls. and three adj3 pin SAME ((integrat\$3 adj3 circuit) or IC)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/13 10:37
S19 2	503	three adj3 pin SAME ((integrat\$3 adj3 circuit) or IC)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/13 10:37
S19 3	201	three adj3 pin WITH ((integrat\$3 adj3 circuit) or IC)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/13 10:38
S19 4	2	three adj3 pin WITH ((integrat\$3 adj3 circuit) or IC) SAME (sound or audio or loudspeaker or speaker)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/13 10:37

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